



Docket No.: 49657-904

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Yoshinori TANAKA, et al. :
Serial No.: 09/756,864 : Group Art Unit: 2826
Filed: January 10, 2001 : Examiner: T.L. Dickey
For: SEMICONDUCTOR DEVICE COMPRISING A HIGHLY-REL
CAPACITANCE CAPACITOR (AS AMENDED)

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, DC 20231

Siri:

Prior to examination of the above-referenced application with respect to the present RCE, please amend the application as follows:

IN THE SPECIFICATION:

Please amend the first line of the specification to read:

This application is a Divisional of Application Serial No. 09/095,612 filed June 11, 1998
now Patent No. 6,194,758.

IN THE CLAIMS:

Please amend claim 12 as follows. A clean copy of the claim is appended hereto.

12. (Twice Amended) A semiconductor device including a memory cell region and a peripheral circuit region, comprising: